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SERIAL NUMBER 10/624,398	FILING DATE 07/22/2003 RULE	CLASS 438 716/11	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. SC12862TC
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APPLICANTS

Leo Mathew, Austin, TX;

Byoung W. Min, Austin, TX;

 ** CONTINUING DATA *N VS* *****

 ** FOREIGN APPLICATIONS *N VS* *****

 IF REQUIRED, FOREIGN FILING LICENSE
 GRANTED

** 03/25/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY TX	SHEETS DRAWING 5	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	Verified and Acknowledged	<i>Under Seal</i> Examiner's Signature	<i>VS</i> Initials	

ADDRESS

23125
 FREESCALE SEMICONDUCTOR, INC.
 LAW DEPARTMENT
 7700 WEST PARMER LANE MD:TX32/PL02
 AUSTIN , TX
 78729

TITLE

Method for converting a planar transistor design to a vertical double gate transistor design

FILING FEE RECEIVED 750	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____
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